

# Thomas Noulis

## Associate Professor

### Contact

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### Skills

Analog/RF IC Design  
Mixed Signal IC Design  
Design Methodology and EDA  
Measurement and Testing  
Project Management

### Languages

Greek  
English  
German

### Resume

Prof. Thomas Noulis is an Academic and a Semiconductor Industry expert (INTEL Corp., INFINEON Semiconductors AG, HELIC Inc. (acquired by ANSYS/ acquired by Synopsis)) with more than 20 years of international experience in Design System Development, Analog/RFMS circuit Design, Design Methodology and international project management.

He holds B.Sc. Degree in Physics (2003), MSc. Degree in Electronics Engineering (2005), and a Ph.D. (2009) from Aristotle Univ. of Thessaloniki (collaboration with LAAS, Toulouse-France).

He has participated in Integrated Circuit Design European and National research projects. He has been in his industrial career, with HELIC R&D on Signal and Power integrity EDA tools development, INTEL wireless platform R&D and with INFINEON automotive power technologies platform.

Dr. Noulis is the author of more than 100 publications, holds one European patent, 8 book chapters and he is the Editor/Co-editor of 4 scientific books. He is an active reviewer and a member of the editorial board of international scientific journals and conferences.

He has given numerous invited presentations on Mobile Communications ICs, Signal and Noise integrity in SoCs, Analog IC design towards AI enablement and Readout from end IC design. His research interests are focused on Low noise circuit design, RF and millimeter Wave IC design, Design Methodology and EDA, Signal and Power Integrity, Instrumentation and sensor interfaces.

Currently he is an Associate Professor in the Physics Dept. of Aristotle Univ. of Thessaloniki, Greece, leading a team of 10 people on ASIC design and Design Automation and running multiple Academic and Industrial research projects.