

Short CV

Name	Konstantinos Siozios
<i>Position</i>	Associate Professor, School of Physics, AUTH (ΦΕΚ: 1672/26-7-2021 τ. Γ')
<i>Studies</i>	<ul style="list-style-type: none"> • PhD, Department of Electrical and Computer Engineering, Democritus University of Thrace (2009) • Master, Department of Electrical and Computer Engineering, Democritus University of Thrace (2003) • Bachelor, Department of Electrical and Computer Engineering, Democritus University of Thrace (2001)
<i>Scientific expertise</i>	<ul style="list-style-type: none"> • Assistant Professor, School of Physics, AUTH (2016-now) • Adjunct Lecturer at Technological Educational Institute of Athens (2014–2016) • Adjunct Lecturer at Technological Educational Institute of Piraeus (2014) • Adjunct Lecturer at Dept. of Information Technology and Telecommunication, University of Western Macedonia (2012) • Adjunct Lecturer at Technological Educational Institute of Chalkida (2005-2007) • Adjunct Lecturer at Technological Educational Institute of Western Macedonia (2005-2007) • Reviewer at 28 international transactions and journals (IEEE, ACM, Elsevier)
<i>Research activities</i>	<p>Research activities:</p> <ul style="list-style-type: none"> • Digital design for reconfigurable architectures • Hardware/Software co-design • Accelerate computational intensive tasks with reconfigurable architectures • CAD algorithms • Rapid virtual prototyping of digital circuits • CyberPhysical Systems <p>Participate to Wireless and Photonic Systems and Networks (WinPhoS) research group at AUTH's Center for Interdisciplinary Research and Innovation (http://winphos.web.auth.gr/).</p> <p>Participation at Research Projects:</p> <ul style="list-style-type: none"> • 4 projects as Partner • 26 projects as Primary Investigator

	<p>The research outcomes is published at:</p> <ul style="list-style-type: none"> • 3 scientific books • 7 book chapters • 37 publications in peer reviewed transactions & journals • 102 publications in international conferences <p>Additional research activities:</p> <ul style="list-style-type: none"> • Supervise 3 PhD Thesis • Supervise 7 Master Thesis • Supervise 20 diploma thesis • Member of the organizing committee in 34 international conferences • Organization of 2 summer schools <p>Detail info can be found at http://users.auth.gr</p>
<p><i>Five most important publications (last 5 years)</i></p>	<ol style="list-style-type: none"> 1. K. Balaskas, G. Zervakis, H. Amrouch, J. Henkel and K. Siozios, "Automated Design Approximation to Overcome Circuit Aging", IEEE Transactions on Circuits and Systems I: Regular Papers, DOI: 10.1109/TCSI.2021.3106149 2. G. Zervakis, K. Koliogeorgi, D. Anagnostos, N. Zompakis and K.Siozios, "VADER: Voltage-Driven Netlist Pruning for Cross- Layer Approximate Arithmetic Circuits," in IEEE Transactions on Very Large-Scale Integration (VLSI) Systems, vol. 27, no. 6,pp. 1460-1464, June 2019. 3. C. Marantos, K. Siozios and D. Soudris, "Rapid Prototyping of Low-Complexity Orchestrator Targeting CyberPhysical Systems: The Smart-Thermostat Usecase," in IEEE Transactions on Control Systems Technology, Vol. 28, No. 5, pp. 1831-1845, Sept. 2020. 4. K. Maragos, G. Lentaris, D. Soudris, K. Siozios and V. F. Pavlidis, "Application performance improvement by exploiting process variability on FPGA devices," Design, Automation & Test in Europe Conference & Exhibition (DATE), 2017, pp. 452-457, Lausanne, 2017. 5. K. Siozios and D. Soudris, "A Customizable Framework for Application Implementation onto 3-D FPGAs," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 35, no. 11, pp. 1783-1796, Nov. 2016. <p>Publication list can be found in http://ksiop.webpages.auth.gr/?page_id=10</p>