

Sort CV

<i>Name</i>	Spyridon Nikolaidis
<i>Position</i>	Professor Expertise: Analysis and design of digital electronic circuits
<i>Studies</i>	<ul style="list-style-type: none"> • PhD Degree, Department of Electrical Engineering, University of Patras (1994) • Diploma, Department of Electrical Engineering, University of Patras (1988)
<i>Scientific expertise</i>	<ul style="list-style-type: none"> • Faculty member, Physics Department, 1996 - today • Contract teaching staff, Hellenic Open University, 2003- 2017 • Founder and organizer of the annual International Conference on Modern Circuits and Systems Technologies (MOCASST) from 2012 • Organizer of International Conference on Power, Automation, Timing, Modeling and Simulation (PATMOS) για το 2017 • Member of organizing committee of 3 international conferences • Organizer of three special sessions in international conferences • Editor for the journal ISRN Electronics • 3 awards for presentations in international conferences • Scientific responsible of AUTH for 9 European and National research projects • Management Committee member for Greece in European COST Action IC1401, Memristor: Devices, Models, Circuits and Applications (2014-2018). • Translation and editing of two foreign scientific book • Co-authored a university textbook for Laboratory Exercises
<i>Research activities</i>	<ul style="list-style-type: none"> • 62 publication in international journals • 131 publications in conference proceedings • 3 chapters as co-author in scientific books • More than 720 references from other researchers • Participation as researcher in 20 Research (European and National) projects • Reviewer in many international journals conferences • Invited speaker in international conferences and universities • Supervision of 5 doctoral theses and 3 ongoing • Supervised 40 graduate student theses • Supervised 45 undergraduate student dissertations • Research interests: Design of high speed and low power digital circuits, Embedded system design, Power consumption models for digital circuits and systems, Models of the operation of CMOS logic gates.
<i>Five most important publications</i>	<ol style="list-style-type: none"> 1. I. Messaris, A. Serb, S. Stathopoulos, A. Khat, S. Nikolaidis, T. Prodromakis, "A Data-Driven Verilog-A ReRAM Model," IEEE Transactions on CAD, 2018. 2. C.L Sotiropoulou, L. Voudouris, Ch. Gentsos, A. Demiris, N. Vassiliadis, S. Nikolaidis, "Real-Time Machine Vision FPGA Implementation for Microfluidic Monitoring on Lab-on-Chips" IEEE Transactions on Biomedical Circuits and Systems (TBioCAS), 2013,

DOI: 10.1109/TBCAS.2013.2260338.

3. V. Konstadakos, A. Chatzigeorgiou, **S. Nikolaidis**, Th. Laopoulos, "Energy Consumption Estimation in Embedded Systems," IEEE Transactions on Instrumentation and Measurement, Vol. 57, No 4, pp. 797-804, April 2008.
4. N. Vassiliadis, G. Theodoridis, and S. Nikolaidis, "An Application Development Framework for ARISE Reconfigurable Processors," ACM Transactions on Reconfigurable Technology and Systems, Volume 2, Issue 4, No 24, pp. 24:1-24:30, Sept. 2009.
5. L. Bisdounis, **S. Nikolaidis**, O. Koufopavlou, "Analytical Transient Response and Propagation Delay Evaluation of the CMOS Inverter for Short-channel Devices," IEEE Journal of Solid-State Circuits, Vol. 33, No 2, pp. 302-306, Feb. 1998.