

Curriculum Vitae

Name	Stylios SISKOS
<i>Position</i>	Professor
<i>Studies</i>	<ul style="list-style-type: none"> • Bachelor (Ptyhion) in Physics, Aristotle Univ. of Thessaloniki (1980) • DEA option “Electronique”, Université Paul Sabatier de Toulouse (1981) • Doctorat 3eme cycle option “Electronique”, Université Paul Sabatier de Toulouse (1983)
<i>Scientific Experience</i>	<p><u>Positions</u></p> <p>1985-1989: Dept. of Electrical Engineering, Aristotle Univ. of Thessaloniki, <i>Lecturer</i> (under contract, Law 407/1980)</p> <p>1985-1989 : Technological Institute of Thessaloniki., Dept. of Electronics, <i>Professor (contract)</i></p> <p>1989-1993 : Dept. of Physics, Aristotle Univ. of Thessaloniki, <i>Lecturer</i></p> <p>1993-2003: Dept. of Physics, Aristotle Univ. of Thessaloniki, <i>Assistant Professor (Permanent from 1998)</i></p> <p>2003-2011 : Dept. of Physics, Aristotle Univ. of Thessaloniki, <i>Associate Professor</i></p> <p>May 2011 – today : Dept. of Physics, Aristotle Univ. of Thessaloniki, <i>Professor</i></p> <p>2003-2005, 2007-2010, 2013-2014, 2017-2019 Director of the Section of Electronics and Computers</p> <p>2013-2019 Director of Master Program on Electronic Physics</p> <p>2011-2019 Director of Electronics Laboratory</p> <p><u>Member of international organizations</u></p> <p>IEEE Senior Member</p> <p><u>Conference organization</u></p> <p>Co-chair of the 2nd Pan-Hellenic Conference on Electronics and Telecommunications (PACET 2102)</p> <p>Co-organiser of the IEEE CASS Seasonal School on “Wireless Sensor Networks - Wise School 2016”, Thessaloniki, 15-18 Dec., 2016</p> <p>Workshop on “Bioelectronics” on Dec. 15-16, 2017</p> <p>Co-organiser of the IEEE CASS Seasonal School "IoT Bulding Blocs", Thessaloniki, 24-25 Nov. 2017</p>
<i>Research activities</i>	<p><u>Publications-Citations-Patents</u></p> <p>3 book chapters</p> <p>Co-author of a book "Laboratory Experiments in Electronics"</p> <p>61 in international journals and 140 international conferences</p> <p>More than 900 citations</p> <p>3 patent</p> <p>1 patent pending</p>

	<p><u>Research programs</u> Coordinator or participant in 18 National and European research programs and 7 Bilateral collaborations</p> <p><u>Educational programs</u> 3 ERASMUS agreements Participant in 2 national programs for the curriculum restructuration of the Physics Dept.</p> <p><u>Reviewer</u> In the main journals and scientific conferences in the field of Electronics</p> <p><u>Theses Supervisor</u> 30 final studies theses 35 MSc theses 5 PhD theses 3 PhD theses (in progress)</p>
<i>Research interests</i>	<p><u>Design of Analog/Mixed signal of Integrated Circuits</u></p> <ul style="list-style-type: none"> - Read-out circuits for sensor applications - Current mode integrated circuits design - RF integrated circuits design - Design of integrated circuits for signal processing - γ, n dosimetry using MOSFETs detectors - Circuits design using TFT LCD and OLED displays - High data rate A/D and D/A design - Design of interfacing circuits for energy harvesting applications
<i>Important Scientific publications</i>	<ol style="list-style-type: none"> 1. A reconfigurable storage unit for autonomously powered IoT nodes V Gogolou, S Siskos, T Laopoulos, 2019 Panhellenic Conference on Electronics & Telecommunications (PACET), 1-4, Volos, Greece 2019 2. Athanasiadis, Pavlos, Mountrichas, Lampros; Siskos, Stylianos, "An Ultra Wide-Band Adaptive Frequency Divider For mm-wave PLL Applications", IEEE NORCAS 2015 3. L. Mountrichas and S.Siskos, "A high-speed offset cancelling distributed sample-and-hold architecture for flash A/D converters", Microelectronics Journal, Volume 44, Issue 12, December 2013, Pages 1123-113 4. V. Kalenteridis, L.Mountrichas, S.Vlassis, S.Siskos, "A CMOS linear-in-dB VGA and AGC loop for telecommunication applications", Microelectronics Journal, Volume 44, Issue 12, Dec. 2013, pp. 1063- 5. Kalenteridis, V.; Vlassis, S.; Siskos, S., "1.5-V CMOS exponential current generator", Analog Integrated Circuits and Signal Processing 72(2), 6. G. Sarrabayrouse, S. Siskos, "Low dose measurement with thick gate oxide MOSFETs", Radiation Physics and Chemistry (November 2011) doi:10.1016/j.radphyschem.2011.11.020 7. I. Pappas, S. Siskos and C. A. Dimitriadis, "A New Threshold Voltage Compensation Technique of poly-Si TFTs for AMOLED Displays Pixel Circuits ", Journal of the Society for Information Display (SID), 18/10, pp. 721-731, 2010 8. S. Siskos "Low voltage analog median filters implementation" IEEE Intern. Conf. on Imaging Systems and Techniques (IST 2010), July 1-2, 2010, Thessaloniki, Greece